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November 22, 2006

From: Curtis A. Vock

Matter No.: 408204

To:	Company:	Fax Number:	Phone Number:
Mail Stop: Amendment Commissioner for Patents	U.S. Patent Office	(571) 273-8300	

Number of Pages Transmitted (including this cover sheet): 19

## Message:

Applicant(s)	Efren M. Lacap et al.
Serial No.	10/648,586
Filed	26 August 2003
For	WAFER-LEVEL CHIP SCALE PACKAGE

Examiner	Jonathan J. Johnson
Group Art No.	1725
Confirmation No.	4089

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Applicant(s): Efren M. Lacap, et al.			408204
Serial No.	Filing Date	Examiner	Group Art Unit
10/648,586	August 26, 2003	Jonathan J. Johnson	1725
Invention Wafer-Level Chip Scale Package			
<p>I hereby certify that the following: Facsimile Cover Sheet (1 page); Transmittal Form (1 page); Petition for Extension of Time Under CFR 1.136(a) (1 page, in duplicate); Amendment And Response (14 pages); authorization to charge Deposit Account No. 12-0600 in the amount of \$510.00.00 for the extension of time fee; authorization to charge additional fees that may be required, or credit any overpayment, to Deposit Account No. 12-0600; are being sent via facsimile transmission to Mail Stop: Amendment, c/o the Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 22nd day of November 2006</p>			
<p>_____ Janice Price Name of Transmitter</p> <p><i>Janice Price</i> Signature of Transmitter</p>			
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PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031  
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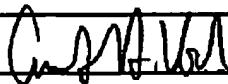
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<b>TRANSMITTAL FORM</b>		Application Number	10/648,586
		Filing Date	August 26, 2003
		First Named Inventor	Efren M. Lacap
		Art Unit	1725
		Examiner Name	Jonathan J. Johnson
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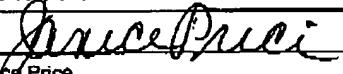
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		<b>Remarks</b>			

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Date	November 22, 2006	Reg. No.	38,356

**CERTIFICATE OF MAILING 37 CFR 1.10**

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